

FIG·2

2 / 10

67

PERFORMANCE OF CHANNEL CODEC ROUTINES

| routines | DSP utilisation | C5x proc. protocol pp utilisation |
|--|----------------------------|---------------------------------------|
| 16 bit CRC identification | 6 instr/bit 5 instr/bit | 4 instr/bit 1 instr/bit |
| RATIO | | |
| sel/instr efficiency no. of trans. MIPS | *1 58 KTx 28 MIPS | *2,2 6,5 KTx 28*2,2=62 MIPS DSP |

<u>FIG · 3</u>

PERFORMANCE OF MODEM ROUTINES

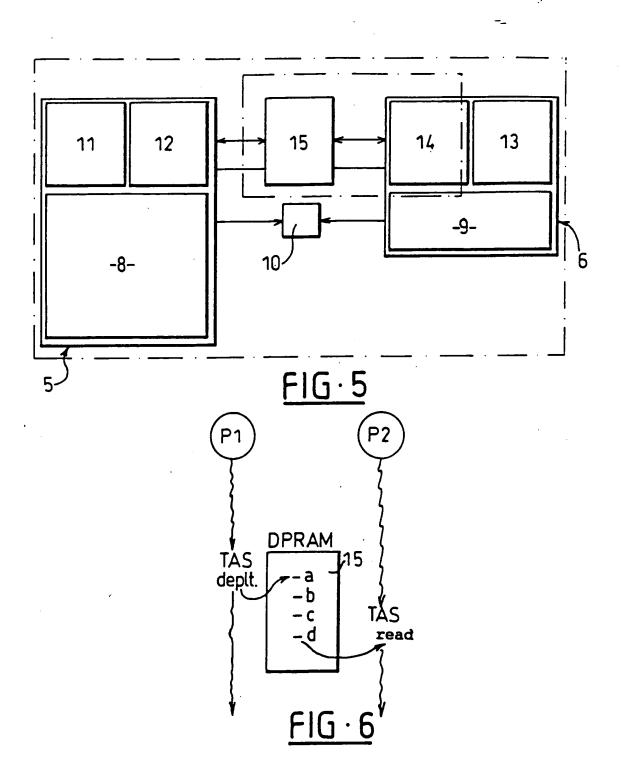
| routines | DSP utilisation C5 | array proc. |
|---|--------------------|-------------|
| metric computation 57 symbols (4 samples) | 43800 cycles | 4400 cycles |
| | | |

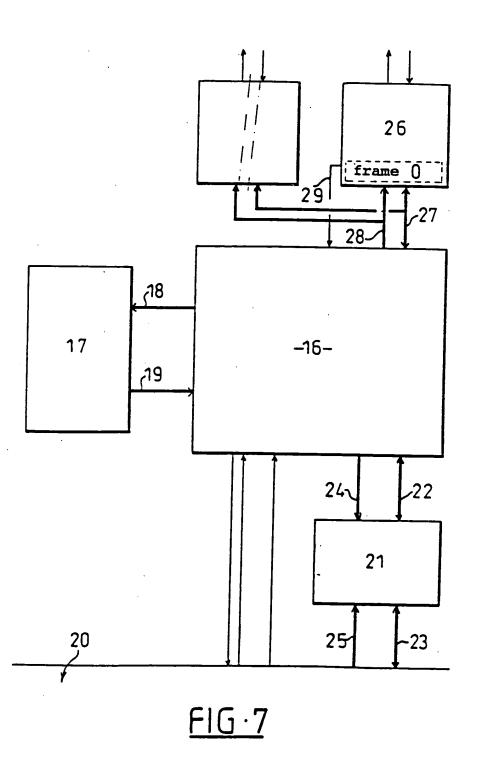
| RATIO | | |
|--|---------------|----------------------------|
| instruction setting efficiency MIPS | ≆1 28 MIPS | *10 28*10= 280 MIPS DSP |

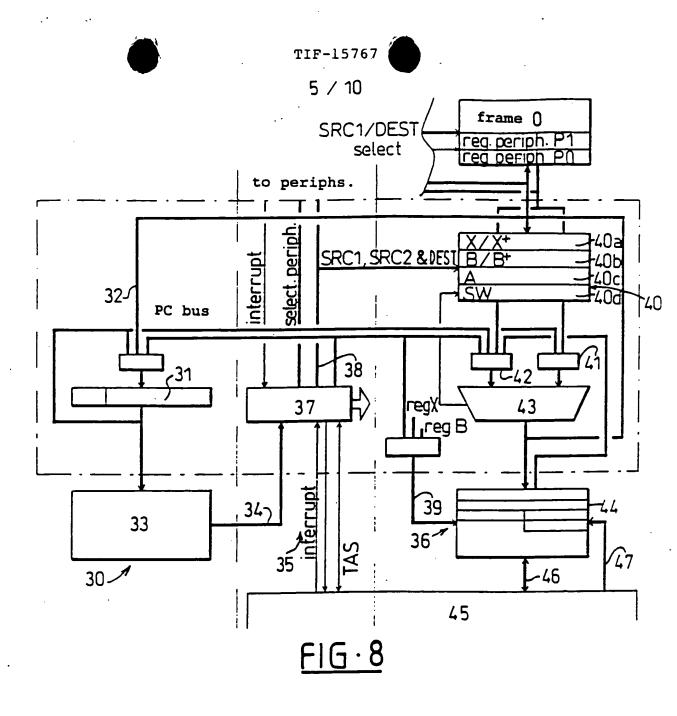
<u> FIG -4</u>

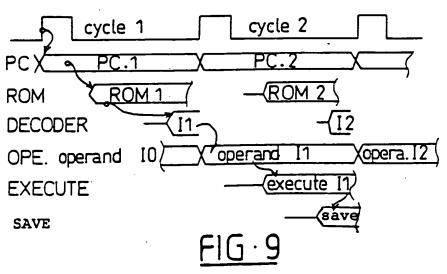
TIF-15767

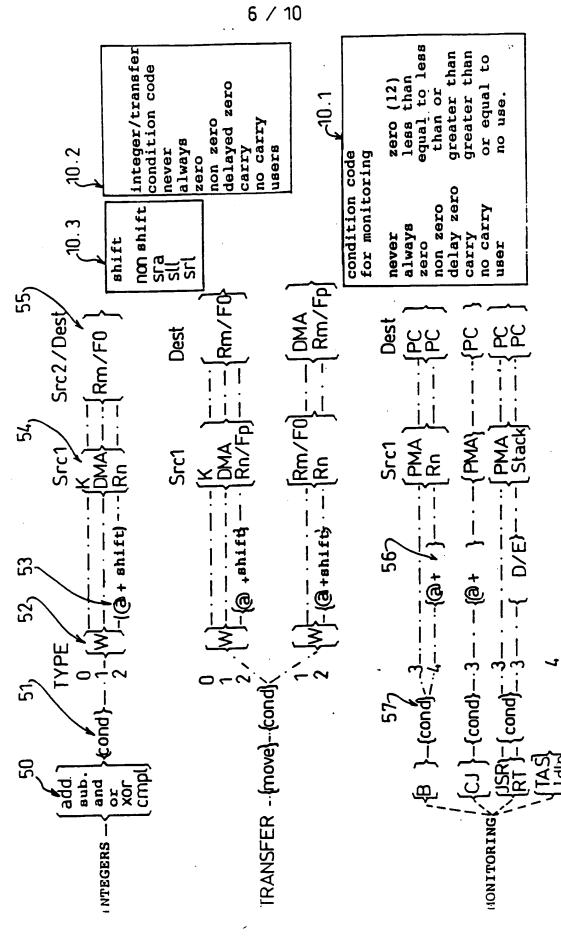
.3 / 10



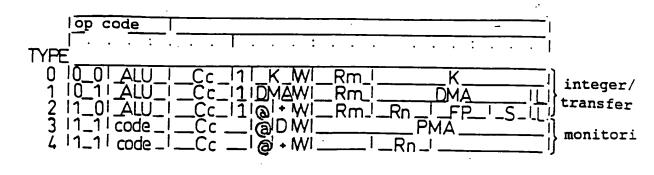








7 / 10

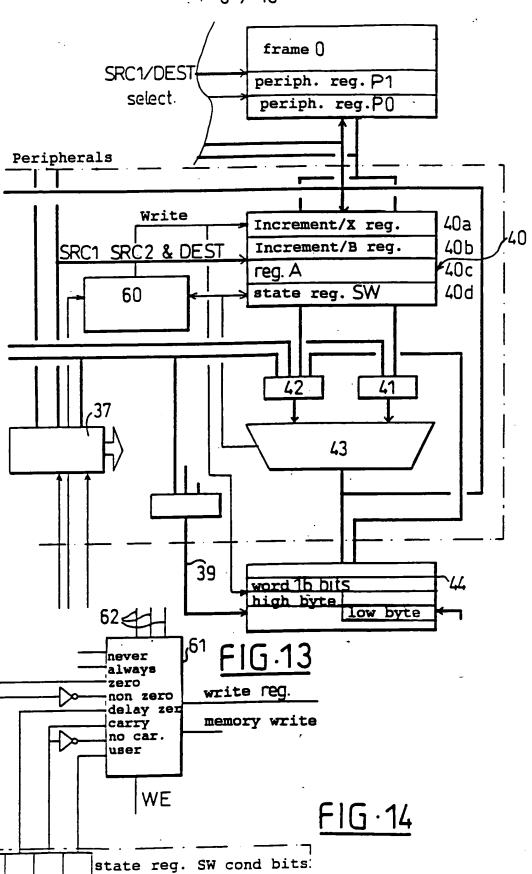


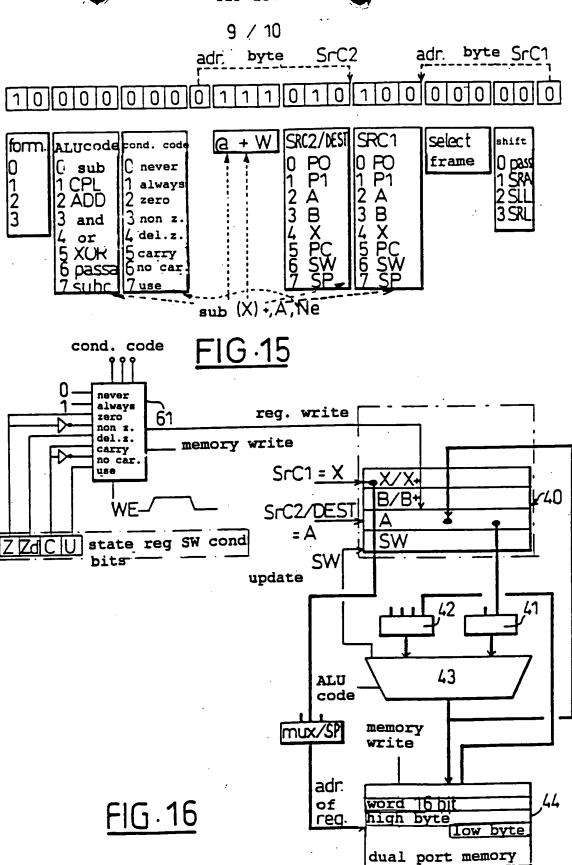
FIG·11

| ALU | Code | Сс | |
|--|---|---|---|
| 0 - sub 1 - CPL 2 - add 3 - and 4 - or 5 - XOR 6 - PASSA 7 - SUBC | 0 - ST type 1 1 - ST type 2 2 - B type 3 3 - B type 4 4 - CALL 5 - RTS 6 - RTI 7 - STOP | 0 - never 1 - always 2 - Z 3 - NZ 4 - ZD 5 - C 6 - NC 7 - user | 8 - Z12 9 - LO 10 - LE 11 - G 12 - GE 13 - NU 14 - (BL) 15 - |
| Rm/Rn 0 - P0 1 - P1 2 - A 3 - B 4 - X 5 - PC 6 - SW 7 - SP | W 0 - R/W byte 1 - R/W word | L 0 - Rm low 1 - Rm high L 0 - DMA/Rn low 1 - DMA/Rn high | S 0 - PASS 1 - SRA 2 - SLL 3 - SRL |

FIG · 12

8 / 10





TIF-15767

